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Year	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100
1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095	2096	2097	2098	2099	2100	

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7. The drive circuit as specified in Claim 6 wherein the voltage signal varies proportionally to the piezo actuator load.

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8. The drive circuit as specified in Claim 1 wherein the drive amplifier has a feedback, wherein the sensing circuit is a portion of the feedback.

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9. The drive circuit as specified in Claim 5 wherein the signal is indicative of the piezo actuator load variation.

10. The drive circuit as specified in Claim 1 further comprising a current mirror selectively coupled to the output of the drive amplifier.

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11. The drive circuit as specified in Claim 10 wherein the current mirror is selectively uncoupled from the drive amplifier in the sensing mode.

12. The drive circuit as specified in Claim 11 wherein the current mirror is a class AB amplifier.

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13. The drive circuit as specified in Claim 1 wherein the drive amplifier has a charge mode feedback configured to allow multiple piezo actuators to be driven in the charge mode.

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14. The drive circuit as specified in Claim 13 wherein the charge mode feedback includes a DC restore amplifier forming a portion of the sensing circuitry.

5 15. The drive circuit as specified in Claim 14 wherein the DC restore amplifier is reconfigured in the sensing mode.

10 16. The drive circuit as specified in Claim 15 wherein the reconfigured DC restore amplifier is connected in a closed feedback loop in the charge mode, and in an open feedback loop in the sensing mode.

17. The drive circuit as specified in Claim 1 wherein the drive amplifier has a first output, and a second output having a current mirror based on the first output.

15 18. The drive circuit as specified in Claim 17 wherein a capacitor is coupled to the first output and the piezo actuators are adapted to be driven by the second output.

20 19. The drive circuit as specified in Claim 18 wherein a first time constant formed by the capacitor and the voltage mode feedback, and a second time constant formed by the piezo actuators and the voltage mode feedback, are substantially equal.

25 20. The drive circuit as specified in Claim 13 further comprising a DC control circuit controlling the DC value at the piezo actuator.

21. The drive circuit as specified in Claim 1 wherein the DC control circuit is integrated into the low frequency compensation loop.

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22. The drive circuit as specified in Claim 1 further comprising a digital-to-analog (DAC) coupled to one drive amplifier input and a voltage reference being coupled to another drive amplifier input.

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23. The drive circuit as specified in Claim 1 further comprising an ADC coupled to the sensing circuit.